Amendments to the Specification:

Please replace the paragraph on page 4, starting at line 17 with the following amended paragraph:

FIGURE 1 further includes a control portion 19 which implements a hardware control loop according to the invention. The hardware control loop receives RSSI information from the IF amplifier at 16, and provides in response thereto a high/low gain switching signal 8 for switching the LNA between low gain operation and high gain operation. The logic control portion 19 also provides at 7 a digital signal which is converted into analog format by an ~~A/D~~ D/A converter 18 whose output provides a filter gain control signal to the amplifiers of the channel filters. The logic control portion 19 receives from the baseband processor information at 10 indicative of the desired bias level of the IF amplifier 15, and also receives from the baseband information at 9 indicative of a threshold power level above which the LNA should be switched to low gain operation and below which the LNA should be switched to high gain operation. The logic control portion 19 also provides to the baseband processor the RSSI information received at 16 from the IF amplifier 15. The baseband processor can also perform other well-known conventional functions in support of the communication operation of the receiver of FIGURE 1.